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PATENT
(5298-04700/PM00028)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Gilboa et al.

Serial No. 09/846,119

Filed: April 30, 2001

For: METHOD OF MAKING A
PLANARIZED SEMICONDUCTOR
STRUCTURE

Group Art Unit: 1763
Examiner: Goudreau, G.

Atty. Dkt. No. 5298-04700

I hereby certify that this correspondence is being deposited
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Mail in an envelope addressed to: **Box: Non-Fee Amendment,**
Commissioner for Patents, Washington, D.C. 20231, on the date
indicated below:

September 19, 2003
Date

Kevin L. Daffer

RESPONSE TO OFFICE ACTION MAILED MAY 21, 2003

Box: Non-Fee Amendment
Commissioner for Patents
Washington, D.C. 20231

Dear Sir/Madam:

This paper is submitted in response to the Office Action mailed May 21, 2003 to further highlight reasons why the application is in condition for allowance.

Remarks begin on page 2 of this paper.

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